

TD/050" 6684460

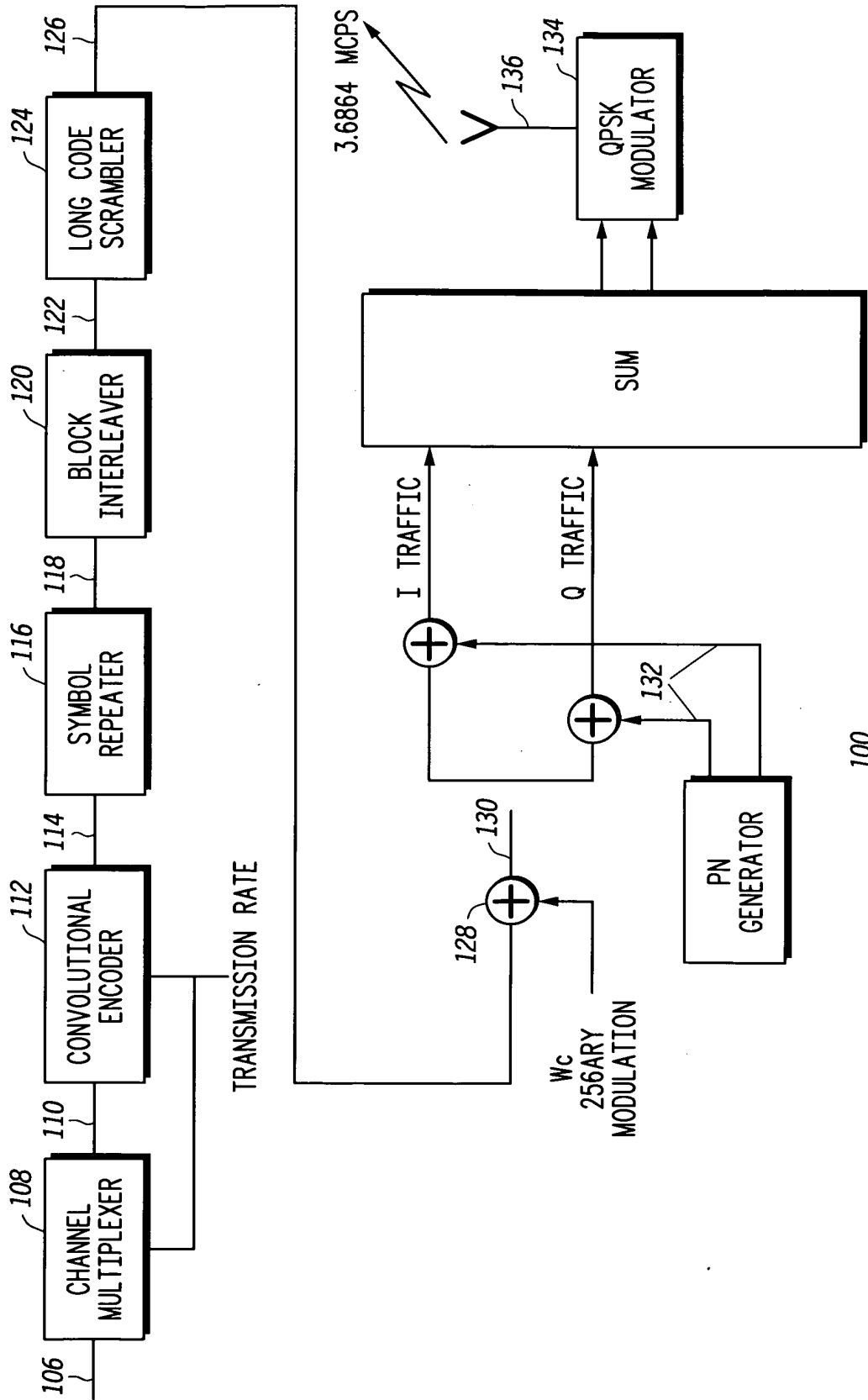
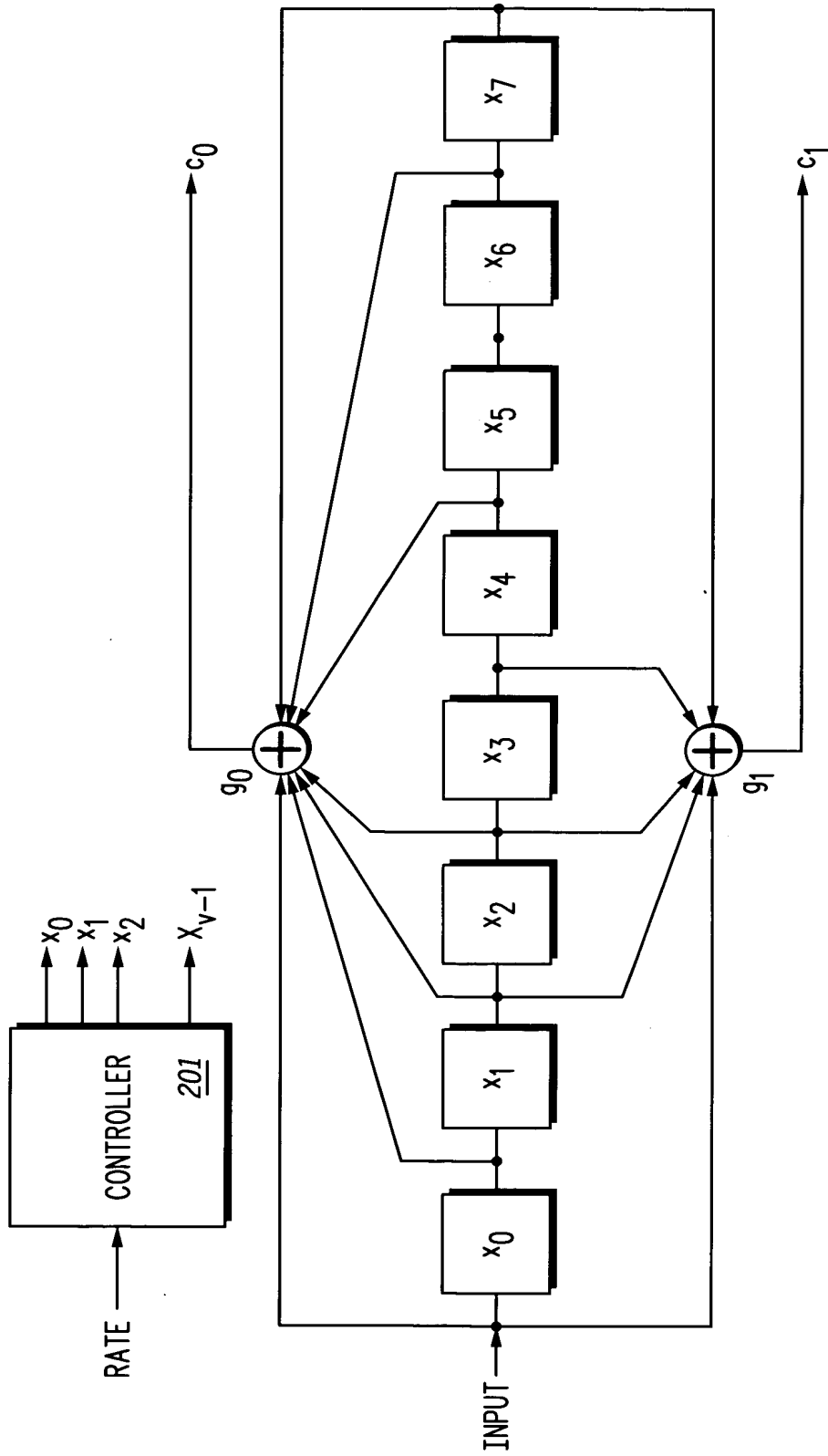


FIG. 1

100



112

FIG. 2

10/05/00 E6842/60

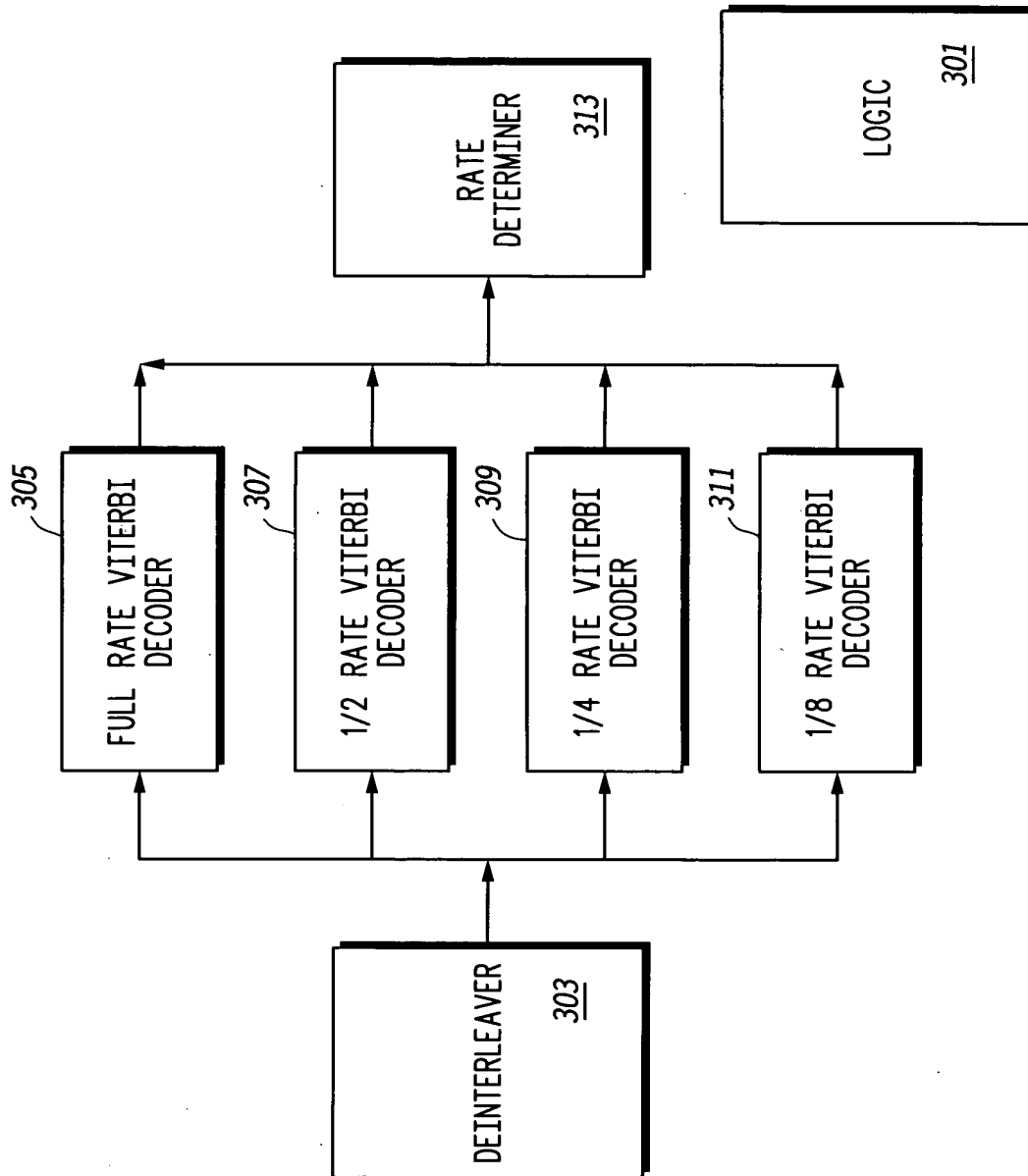
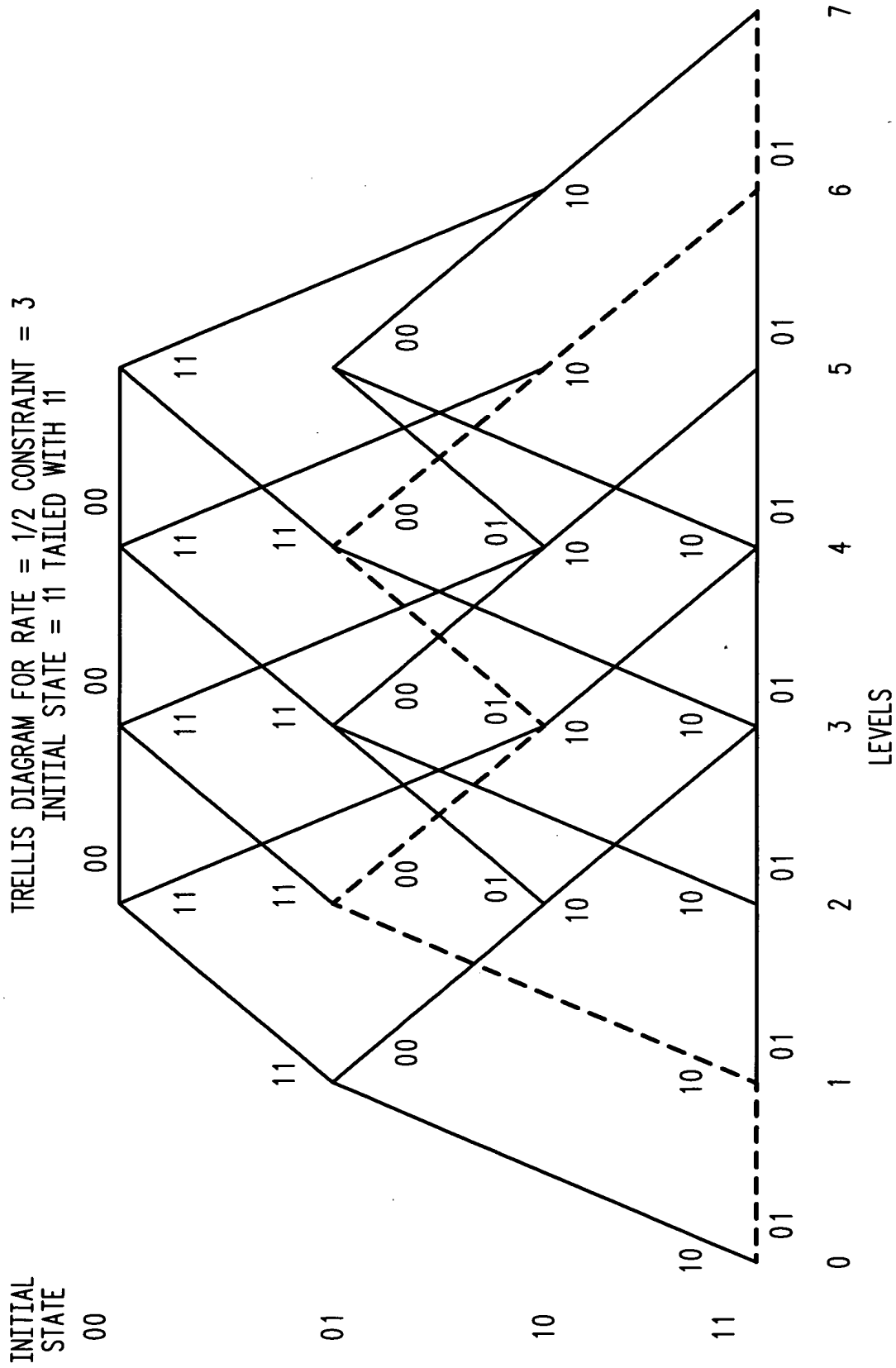


FIG. 3

TD2050" E6B42260

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TRELLIS DIAGRAM FOR RATE = 1/2 CONSTRAINT = 3
INITIAL STATE = 11 TAILED WITH 11



RECEIVED SEQUENCE = 01100001011001
DECODED SEQUENCE = 1010111

FIG. 4

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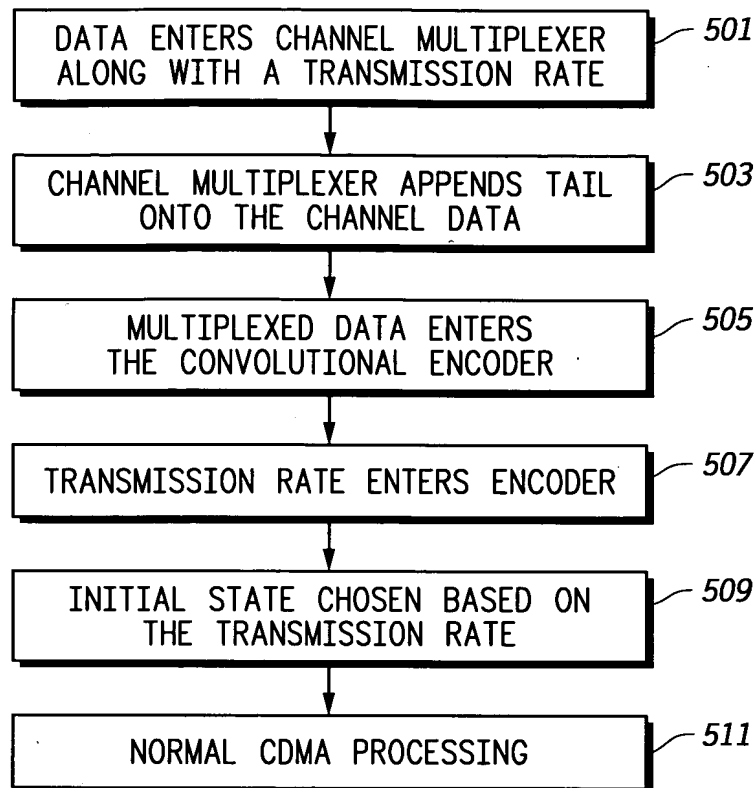


FIG. 5

... 101100101011110100011010101010
DATA TAIL BITS

FIG. 6

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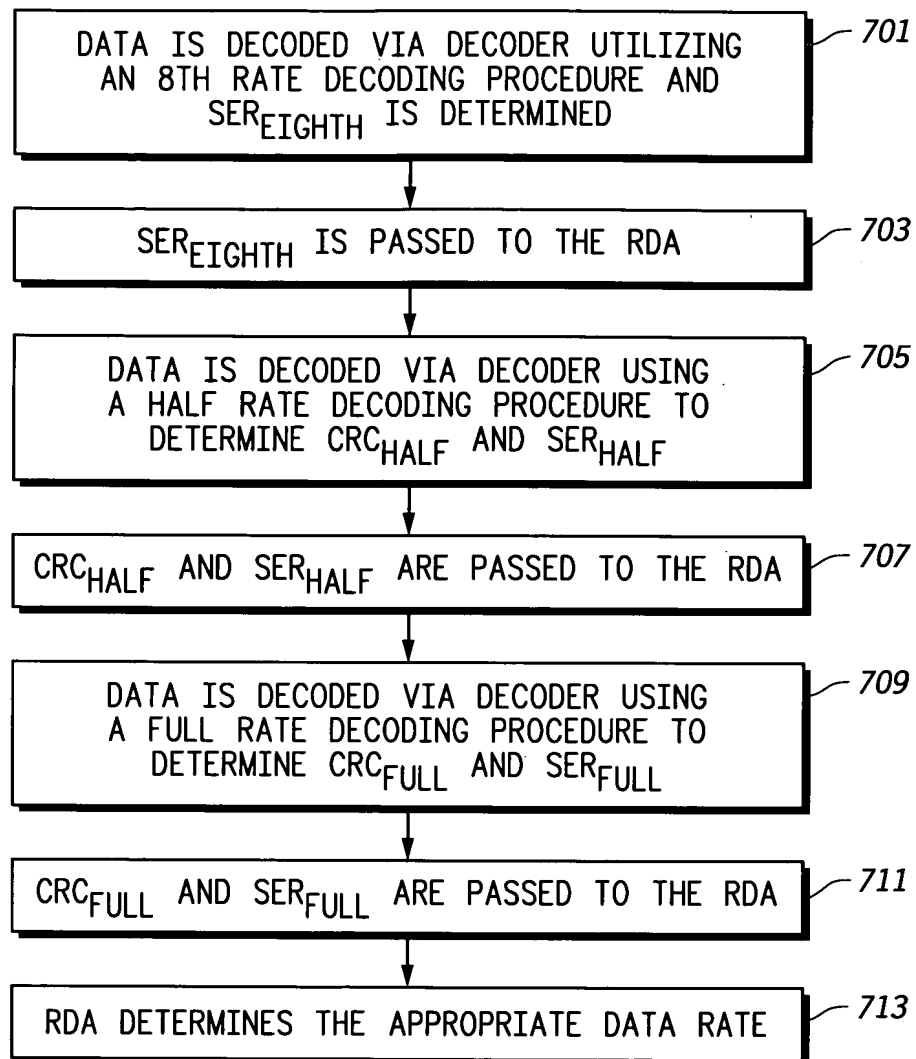


FIG. 7

0974893.050704